81754.0118 Express Mail Label No. EV 324 112 455 US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

the application of: Terunao HANAOKA of Nagano-ken, Japan

Serial No: Not assigned

Filed: March 17, 2004 For:

Semiconductor Device and Method for Manufacturing the Same, Circuit

Board and Electronic Equipment

Mail Stop PATENT APPLICATION

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Dear Sir:

CORRESPONDENCE ADDRESS:

000026021

				•						
Transmitted herewith for filing is the patent application identified above.										
		CAL	CULA	TION OF FEES						
	ITEM	TOTAL NO. OF CLAIMS		NO. OF CLAIMS OVER BASE	LG/SM \$ ENTITY FEE		\$ AMOUNT	\$ FEE		
Α	TOTAL CLAIMS FEDE	20	-20	0	LG=\$18 SM=\$9	\$18	\$ 0			
В	INDEPENDENT CLAIMS FEE*	3	φ	0	LG=\$86 SM=\$43	\$86	\$ 0		S. S. Asia	
С	C SUBTOTAL - ADDITIONAL CLAIMS FEE (ADD FINAL COLUMN IN LINES A + B)									
D	MULTIPLE-DEPENDENT CLAIMS FEE LARGE ENTITY FEE = \$290 SMALL ENTITY FEE = \$145							Ÿ	6 . 0	
E	BASIC FEE LARGE ENTITY FEE = \$770 SMALL ENTITY FEE = \$385					\$	770			
F	TOTAL FILING FEE (ADD TOTALS FOR LINES C, D, AND E) \$ 770						770			
G	ASSIGNMENT RECORDING FEE \$40 \$40						40			
	*LIST INDEPENDENT CLAIMS 1	•								
 Please charge my Deposit Account No. 50-1314 the amount of \$_0 A copy of this letter is enclosed. A check in the amount of \$_0_ to cover the filing fee is enclosed. A check in the amount of \$_40.00_ to cover Assignment Recordation fee is enclosed. The Commissioner is hereby authorized to charge any deficiency for the following fees associated with this communication or credit any overpayment to Deposit Account No. 50-1314. A copy of this sheet is enclosed. Any additional filing fees required under 37 C.F.R. 1.16 Any patent application processing fees under 37 C.F.R. 1.17 										
				Respectfyll) sub HOGAN & HAR	mitted SON	/ .				

Date: March 17, 2004

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PATENT

Attorney Docket No: 81754.0118

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:		Art Unit:	Not assigned						
Terunao HANAOKA		Examiner:	Not assigned						
Serial No: Not assigned									
Filed: March 17, 2004									
For: Semiconductor Device and Method for Manuf Same, Circuit Board and Electronic Equipmer									
CERTIFICATE OF MAILING "Express Mail" Mailing Labe Date of Deposit:	el No. <u>EV 324</u>	112 455 US							
Mail Stop PATENT APPLICATION Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450									
Dear Sir:									
I hereby certify that ⊠ two copies of a letter of transmittal ⊠ patent application (<u>15</u> page(s) of specification; <u>20</u> claim(s); <u>1</u> page(s) of abstract ⊠ <u>2</u> sheet(s) of formal drawings ⊠ Information Disclosure Statement with <u>2</u> references □ return postcard									
are being deposited with the United States Postal Service "Express Mail Post Office to Addressee service with sufficient postage under 37 C.F.R. § 1.10 on the date indicated above and are addressed to:									
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Date: March 17, 2004		Reynaldo (
Hogan & Hartson, LLP 500 South Grand Avenue, Suite 1900 Los Angeles, California 90071 Telephone: 213-337-6700 Facsimile: 213-337-6701	Nam	Signal	mailing papers cure						